## Po Jui (Elton) Shih

Contact Information	Computer Science Building (K17), Engineering Rd UNSW Sydney, Kensington, NSW, Australia 2052	eshih.pj@gmail.com beeb.page
Research Focus	hardware acceleration, computer architecture, embedded systems, computer networks, bioinformatics	
Education	<ul> <li>University of New South Wales, Sydney, Australia</li> <li>B.Eng. (Class I Honours in Computer Engineering), WAM: 84/100</li> <li>Thesis title: <i>Hardware Accelerated Real-Time Selective Genome Sequencing</i></li> <li>Advisor: <i>Prof. Sri Parameswaran</i></li> <li>Selected Coursework: <i>Digital Circuits and Systems, Computer Architecture</i>, Pasystems, <i>Extended Algorithms and Programming Techniques, Design Project Engineering</i>, Mobile Data Networking</li> </ul>	
Honors and Awards	<ul> <li>First Class Honours, UNSW Faculty of Engineering</li> <li>Outstanding Undergraduate Thesis, UNSW School of CSE (one of 10)</li> <li>Dean's Honours List, UNSW Faculty of Engineering</li> <li>2st Place, UNSW COMP4601: Design Project B (hardware acceleration)</li> <li>1st Place, UNSW COMP3601: Design Project A (hw-sw co-design)</li> <li>1st Place, UNSW COMP3222: Digital Circuits and Systems</li> <li>3rd Place, UNSW COMP2121: Microprocessors and Interfacing</li> </ul>	2021 2021 2018, 2019, 2020 2021 2020 2019 2019
WORK AND Research Experience	Audinate, Sydney, AustraliaResearch Engineer IIResearch Engineer IResearch and Development Engineering InternResearch and Development Engineering InternResearch and Development Engineering InternResearch and Development Engineering InternSchool of CSE, UNSW, Sydney, AustraliaCasual Academic	Aug 2022 - <b>Present</b> Jan 2022 - Aug 2023 Winter 2021 Summer 2020 Summer 2019 Feb 2020 - <b>Present</b>
	<ul> <li>Embedded Systems Research Group, UNSW, Sydney, Australia Undergraduate Researcher</li> <li>Worked on accelerating selective genome sequencing on resource-const through hw-sw co-design [GigaScience 2023]</li> <li>Supervisor: Prof. Sri Parameswaran (co-advised by Dr. Hasindu Gamaa saan Saadat)</li> </ul>	Ũ
Publications	<ul> <li>Peer-reviewed Journal Articles</li> <li>Efficient real-time selective genome sequencing on resource-constrained devices Po Jui Shih, Hassaan Saadat, Sri Parameswaran, and Hasindu Gamaarachchi. </li> <li><i>GigaScience 12 (giad046)</i>, 2023.</li> <li>Dissertation</li> <li>Hardware accelerated real-time selective genome sequencing Po Jui Shih. B.Eng. Honours Thesis, UNSW, 2021.</li></ul>	

Talks	Poster Presentations	
	<b>Efficient real-time selective genome sequencing on resource-constrained devices</b> Po Jui Shih, Hassaan Saadat, Sri Parameswaran, and Hasindu Gamaarachchi.	
	Australian Bioinformatics And Computational Biology Society Conference (ABACBS) 2023, Dec 2023.	
	Efficient real-time selective genome sequencing on resource-constrained devices Po Jui Shih, Hassaan Saadat, Sri Parameswaran, and Hasindu Gamaarachchi. COMBINE Symposium 2023, Dec 2023.	
	Hardware accelerated real-time selective genome sequencing Po Jui Shih.	
	Outstanding Undergraduate Thesis Showcase, UNSW School of CSE, Dec 2021.	
Teaching Experience	<ul> <li>2023 Term 3, COMP3601 Design Project A, Guest lecturer, UNSW</li> <li>2023 Term 2, DESN2000 Eng Design &amp; Prof Practice (COMP), Academic Tutor &amp; Guest lecturer, UNSW</li> <li>2022 Term 3, COMP3601 Design Project A, Course Coordinator &amp; Guest lecturer, UNSW</li> <li>2021 Term 3, COMP3601 Design Project A, Academic Tutor, UNSW</li> <li>2021 Term 2, COMP1521 Computer Systems Fundamentals, Academic Tutor &amp; Lab Assistant, UNSW</li> <li>2020 Term 1, COMP2121 Microprocessors and Interfacing, Academic Tutor, UNSW</li> </ul>	
Advising	Undergraduate Honours Students	
	Katelyn Mak (with H. Gamaarachchi), UNSW, 2023-	
Professional services	External Reviewer: ASP-DAC 2024	
Open-source Software	HARU: A hw-sw co-design for real-time selective sequencing on low-cost edge devices.[Github]sigfish-haru: A fast selective sequencing software using HARU for acceleration.[Github]RUscripts-R9: An upgraded RUscripts supporting Python3, R9 flowcell, slow5 and more.[Github]HARU-HLS: An early POC for HARU using HLS and client-server architecture.[Github]	
COMPUTER SKILLS	Programming languages: C/C++, VHDL, Verilog, Python, Go Tools: Vivado, Vitis HLS, Quartus, Chisel, PetaLinux, Yocto, Buildroot, Matlab, Wireshark RTOS: Zephyr RTOS, FreeRTOS, ThreadX Microprocessor architectures: ARM, RISC-V, AVR, MIPS, Xtensa Others: eXpress Data Path (XDP), BPF, JTAG & OpenOCD	
OTHER/PERSONAL	Languages: <i>English</i> (native proficiency), <i>Traditional Chinese Mandarin</i> (native proficiency) Citizenship: <i>Australian</i>	